

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

MAIL STOP PATENT APPLICATION  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

CERTIFICATE UNDER 37 CFR 1.10

I hereby certify that this correspondence and the documents referred to as attached therein are being deposited on December 5, 2003 with the United States Postal Service in an envelope as "Express Mail Post Office to Addressee," mailing label No. EV335471017US addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Dec. 5, 2003  
Date

Ari Pramudji  
Signature

Inventor(s): Ian M. Williams; Dat T. Nguyen; Jeffrey Chandler Doughty; and Satish D. Salian

Title: Method For Testing Synchronization and Connection Status of A Graphics Processing Unit Module

Transmitted herewith is the patent application identified above, including:

- ☒ Specification, claims and abstract 40 Total Pages
- ☒ Drawings ☒ Formal ☐ Informal 27 Total Pages
- ☒ Executed Declaration and Power of Attorney
- ☐ Information Disclosure Statement with List
- ☒ Assignment of the Invention to NVIDIA Corporation
- ☒ Assignment Recordation Cover Sheet

FEE CALCULATION

	NUMBER OF CLAIMS FILED	LESS NUMBER PAID BY BASIC FEE	NUMBER OF EXTRA CLAIMS (Not less than zero)	LARGE ENTITY FEE
Basic Fee				\$770.00
Total Claims	24	- 20 = 4	X \$18 =	\$ 72.00
Independent Claims	3	- 3 = 0	X \$86 =	\$ .00
Total Filing Fee Calculation				\$ 842.00

- ☒ The Commissioner is hereby authorized to charge \$842.00 to Deposit Account No. 20-0782/NVDA/P000876/AOP. A duplicate copy of this transmittal is enclosed.
- ☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No.20-0782/NVDA/P000876/AOP. A duplicate copy of this transmittal is enclosed.
- ☒ Please address all future correspondence to:  
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Respectfully submitted,

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